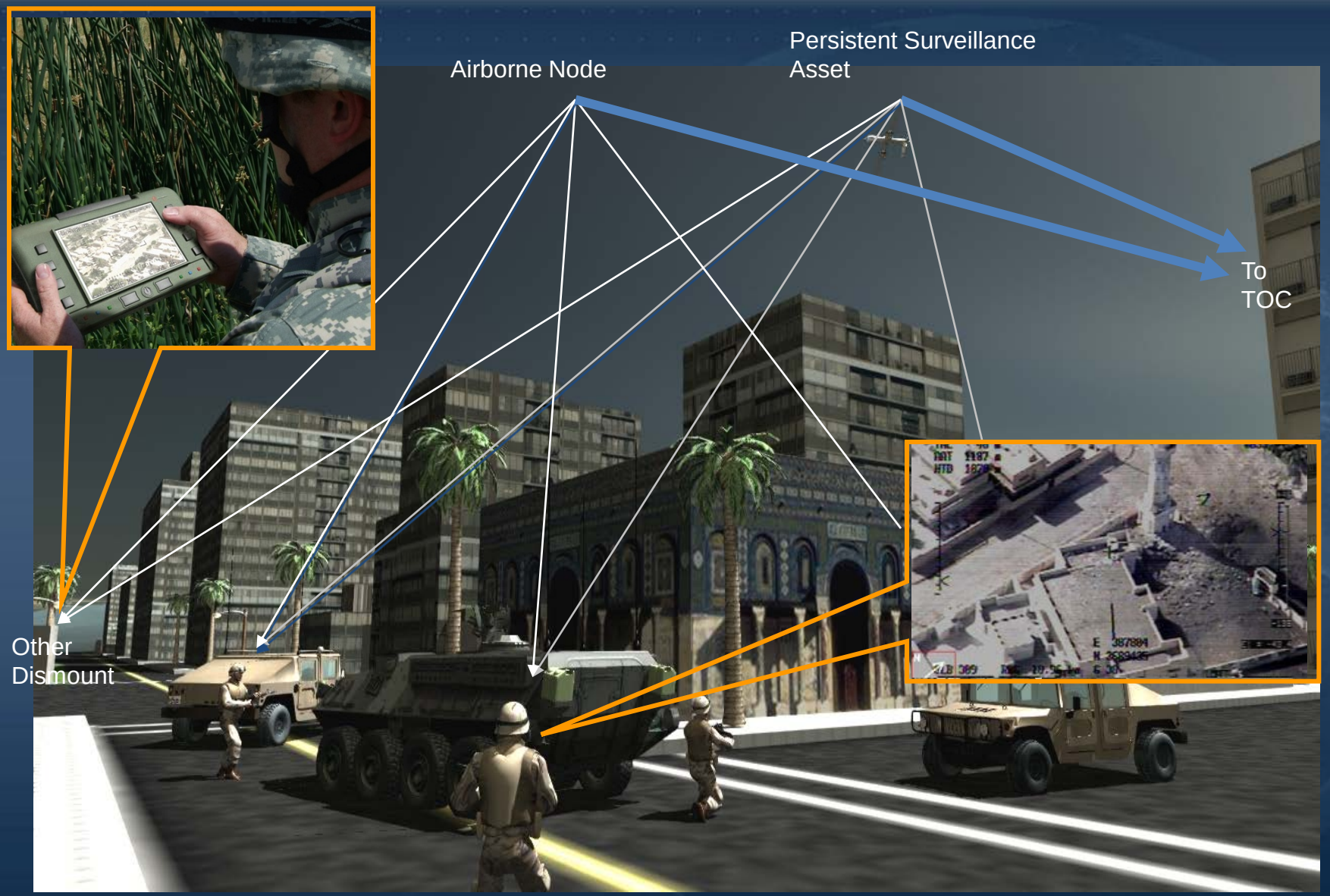
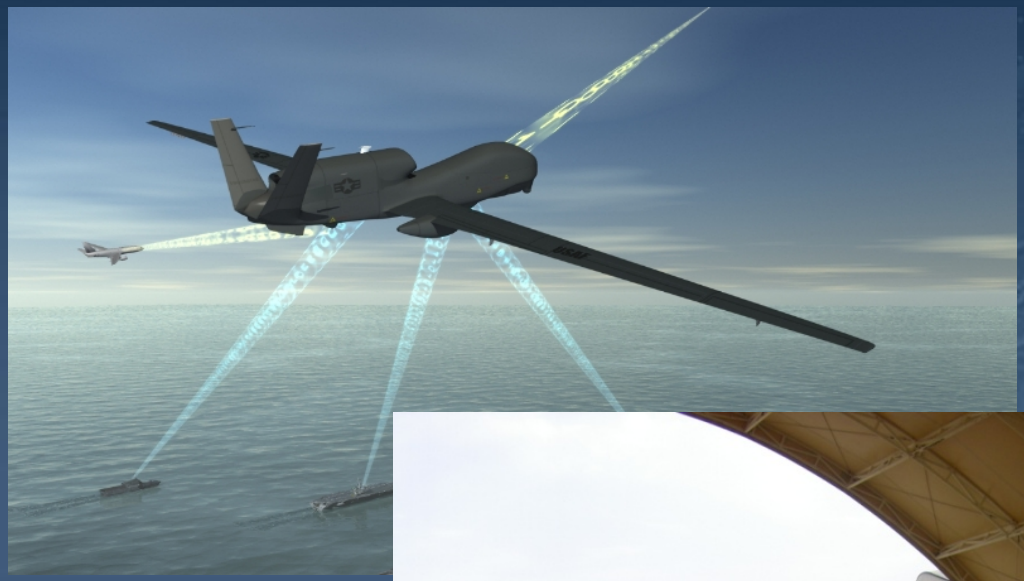
# The Advantages of Maintaining an Effective Preferred Parts Lists

This presentation consists of L-3 Communications Corporation general capabilities information that does not contain controlled technical data as defined within the International Traffic in Arms Regulations (ITAR) Part 120.10 or Export Administration Regulations (EAR) Part 734.7-11.


L3
Communication Systems-West

- ISR Communication Systems
- Tactical and Strategic SATCOM Terminals
- Sustainment Support and Services
- Networking Solutions
- Tactical Pods and Hand-Held Data Links
- Telemetry and UGV Communications
- Integrated Communications


Airborne Node
Persistent Surveillance Asset
To TOC
Other Dismount



Why do we strive to maintain a Preferred Parts List (PPL)?

Improve the Quality of New Designs;
Minimize Costs and Avoid Duplications;
Minimize Lead Times;
Minimize Vanished Vendor (DMS) issues; and
Minimize the Risk of Including Counterfeits in Product.

# Improve the Quality of New Designs

By using the best components we build the best assemblies.

# Minimize Costs and Avoid Duplications

| Pref | ST | SC | VendorPN | BodyDescription | SuffixDescription |
|---|---|---|---|---|---|
| 1 | A | S | SN74LVC244APWR | IC, DGTL-- BFR/DRVR (74LVC244) OCTAL, 3-STATE OUTS, 1.65-3.6V | TSSOP-20, -40/+125 D |
| 1 | A | S | 74LVC244APW,112 | IC, DGTL-- BFR/DRVR (74LVC244) OCTAL, 3-STATE OUTS, 1.65-3.6V | TSSOP-20, -40/+125 D |
| 1 | A | PV | 74LCX2244MTC | IC, DGTL--(74LCX2244) BUFFER OCTAL, W/SERIES RES, 2-3.6V | TSSOP-20, -40 TO +85 |
| 1 | A | PV | 74ALVCH16244GRDR | IC,DGTL--(74ALVCH6244) 16-BIT BUFFER/DRIVER,1.65 TO 3.6V | FBGA-54, -40 TO 85C |
| 4 | A | PV | 74ACT244MTC | IC, DGTL -- BUFFER (74ACT244) OCTAL, 3-STATE OUTPUTS, 5V | TSSOP-20, -40/+85 C, |
| 4 | A | PV | 74ACT244MTC | IC, DGTL -- BUFFER (74ACT244) OCTAL, 3-STATE OUTPUTS, 5V | TSSOP-20, -40/+85 C, |
| 4 | A | PV | MC74ACT244DTR2 | IC, DGTL -- BUFFER (74ACT244) OCTAL, 3-STATE OUTPUTS, 5V | TSSOP-20, -40/+85 C, |
| 4 | A | PV | MC74ACT244DTR2G | IC, DGTL -- BUFFER (74ACT244) OCTAL, 3-STATE OUTPUTS, 5V | TSSOP-20, -40/+85 C, |
| 4 | A | PV | 74VCX16244MTD | IC,DGTL--(74VCX16244) 16-BIT BUFFER/LINE DRIVER,1.2 TO 3.6V | TSSOP-48, -40 TO 85 |
| 4 | A | PV | 74VCX16244MTDX | IC,DGTL--(74VCX16244) 16-BIT BUFFER/LINE DRIVER,1.2 TO 3.6V | TSSOP-48, -40 TO 85 |
| 4 | U | SV | 74ACTQ16244SSC | IC,DGTL--BUFFER/DRVR(CTQ16244) 16-BIT 3-ST BUFFER/LINE DRIVER | -40 TO +85 DEG C, SS |
| 4 | A | PV | 74LCX244WM | IC,DGTL-- BUFFER (74LCX244) LOW V,LINE DRIVER,2-3.6V | SOIC-20, -40 TO 85C, |
| 4 | A | PV | 74LCX244SJ | IC,DGTL-- BUFFER (74LCX244) LOW V,LINE DRIVER,2-3.6V | SOP-20, -40 TO 85C, |
| 4 | A | SV | 74LCX244MSA | IC,DGTL-- BUFFER (74LCX244) LOW V,LINE DRIVER,2-3.6V | SSOP-20, -40 TO 85C |
| 4 | A | PV | 74LCX244MTC | IC,DGTL-- BUFFER (74LCX244) LOW V,LINE DRIVER,2-3.6V | TSSOP-20, -40 TO 85 |
| 4 | A | PV | 74ACT16244DGGR | IC,DGTL--BUFFER (74ACT16244) BUFFER/LINE DRIVER, 16-BIT, 5V | 9.5NS TPD,TSSOP48, |
| 5 | A | PV | IDT74FCT16244ATPV | IC,DGTL-- BUFFER/LINE DRVR (16244) 16-BIT, 5 V | SSOP-48, -40+85C, |
| 5 | A | PV | IDT74FCT16244ATPVG | IC,DGTL-- BUFFER/LINE DRVR (16244) 16-BIT, 5 V | SSOP-48, -40+85C, |
| 5 | A | PV | IDT74FCT244CTSOG | IC, DGTL--(FCT244CT) CMOS OCTAL BUFFER/LINE DRIVER, 5V | 4.1NS TPD,SOIC20,-4 |
| 5 | A | PV | IDT74FCT244CTSO | IC, DGTL--(FCT244CT) CMOS OCTAL BUFFER/LINE DRIVER, 5V | 4.1NS TPD,SOIC20,-4 |
| 5 | U | SV | 74ACTQ244SC | IC, DGTL -- BUFFER (74ACTQ244) OCTAL BUFFER/DRIVER, 5V | SOIC-20, -40 TO +85 |
| 5 | I | S | 74LVC244AD | IC, DGTL-- BFR/DRVR (74LVC244) OCTAL, 3-STATE OUTS, 1.65-3.6V | SOIC-20, -40/+125 C, |
| 5 | I | S | 74LVC244AD,112 | IC, DGTL-- BFR/DRVR (74LVC244) OCTAL, 3-STATE OUTS, 1.65-3.6V | SOIC-20, -40/+125 C, |
| 5 | I | S | 74LVC244AD,118 | IC, DGTL-- BFR/DRVR (74LVC244) OCTAL, 3-STATE OUTS, 1.65-3.6V | SOIC-20, -40/+125 C, |
| 5 | I | S | SN74LVC244ADW | IC, DGTL-- BFR/DRVR (74LVC244) OCTAL, 3-STATE OUTS, 1.65-3.6V | SOIC-20, -40/+125 C, |
| 5 | U | SV | 74VCX16244GX | IC,DGTL--(74VCX16244) 16-BIT BUFFER/LINE DRIVER,1.2 TO 3.6V | 2.5NS, FBGA-54, -40/ |
| 5 | U | SV | 74VCX16244GX | IC,DGTL--(74VCX16244) 16-BIT BUFFER/LINE DRIVER,1.2 TO 3.6V | 2.5NS, FBGA-54, -40/ |
| 5 | U | ZV | 74ACTQ16244MTD | IC,DGTL--BUFFER/DRVR(CTQ16244) 16-BIT 3-ST BUFFER/LINE DRIVER | -40 TO +85C, TSSOP- |
| 5 | U | ZV | 74ACTQ244MSA | IC, DGTL--BUFFER (74ACTQ244) OCTAL BUFFER/DRIVER, 5V | 5V,SSOP-20, -40/+85 |
| 5 | A | PV | 74LCX16244MTD | IC, DIGITAL--LOGIC (LCX16244) LV 16-BIT BUFFER, 2.3V TO 3.6V | 4.5NS, TSSOP-48, -4C |
| 5 | A | PV | MC74LCX16244DT | IC, DIGITAL--LOGIC (LCX16244) LV 16-BIT BUFFER, 2.3V TO 3.6V | 4.5NS, TSSOP-48, -4C |
| 5 | A | PV | IDT74FCT162244ATPV | IC, DIGITAL--(FCT16244) 16-BIT BALANCED BUFFER, 5V | SSOP-48, -40 TO +85 |

By focusing on fewer preferred parts we maximize purchasing power, minimize purchase orders and strengthen the best suppliers.

# Minimize Lead Times

| Pref | ST | SC | VendorPN | BodyDescription | SuffixDescription |
|---|---|---|---|---|---|
| 1 | A | S | SN74LVC244APWR | IC, DGTL-- BFR/DRVR (74LVC244) OCTAL, 3-STATE OUTS, 1.65-3.6V | TSSOP-20, -40/+125 D |
| 1 | A | S | 74LVC244APW,112 | IC, DGTL-- BFR/DRVR (74LVC244) OCTAL, 3-STATE OUTS, 1.65-3.6V | TSSOP-20, -40/+125 D |
| 1 | A | PV | 74LCX2244MTC | IC, DGTL--(74LCX2244) BUFFER OCTAL, W/SERIES RES, 2-3.6V | TSSOP-20, -40 TO +85 |
| 1 | A | PV | 74ALVCH16244GRDR | IC,DGTL--(74ALVCH6244) 16-BIT BUFFER/DRIVER,1.65 TO 3.6V | FBGA-54, -40 TO 85C, |

We can maximize our leverage with suppliers and minimize lead times.

# Minimize Vanished Vendor (DMS) issues

| Pref | ST | SC | VendorPN | BodyDescription | SuffixDescription |
|---|---|---|---|---|---|
| 1 | A | S | SN74LVC244APWR | IC, DGTL-- BFR/DRVR (74LVC244) OCTAL, 3-STATE OUTS, 1.65-3.6V | TSSOP-20, -40/+125 D |
| 1 | A | S | 74LVC244APW,112 | IC, DGTL-- BFR/DRVR (74LVC244) OCTAL, 3-STATE OUTS, 1.65-3.6V | TSSOP-20, -40/+125 D |
| 1 | A | PV | 74LCX2244MTC | IC, DGTL--(74LCX2244) BUFFER OCTAL, W/SERIES RES, 2-3.6V | TSSOP-20, -40 TO +85 |
| 1 | A | PV | 74ALVCH16244GRDR | IC,DGTL--(74ALVCH6244) 16-BIT BUFFER/DRIVER,1.65 TO 3.6V | FBGA-54, -40 TO 85C, |

We can maximize our purchasing of these preferred parts therefore reinforcing the desire of the manufacturer to continue its production.

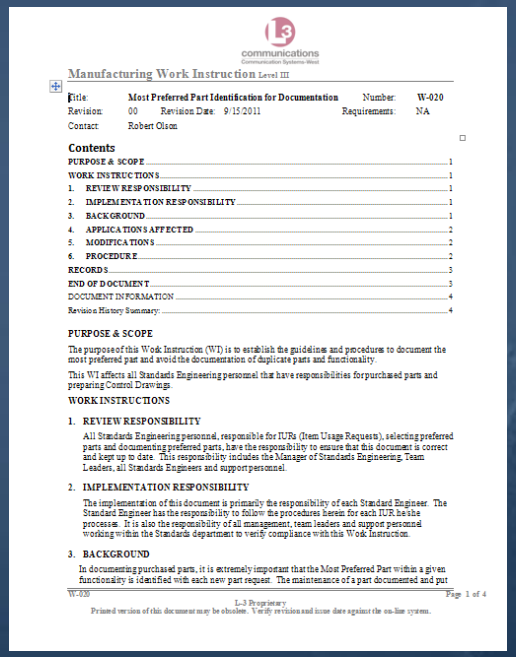
## Minimize the Risk of Including Counterfeits in Product


By focusing on fewer preferred parts we can maximize our purchasing of these parts and reduce the desire of the manufacturer to discontinue its production.

Having an effective PPL requires commitment :

* New Processes and Procedures;
* Aggressive Goals and Timetables;
* Visibility of Real-Time Metrics;
* Open Communications with Design Engineering and Program Management;
* Flexibility; and
* A Highly Refined Taxonomy to compare like parts within a given functionality at the finest degree of granularity.

# New Processes and Procedures



communications
Communication Systems-West

**Manufacturing Work Instruction** Level III

Title: **Most Preferred Part Identification for Documentation** Number: W-020
Revision: 00 Revision Date: 9/15/2011 Requirements: NA
Contact: Robert Olson

**Contents**



**PURPOSE & SCOPE**

The purpose of this Work Instruction (WI) is to establish the guidelines and procedures to document the most preferred part and avoid the documentation of duplicate parts and functionality.

This WI affects all Standards Engineering personnel that have responsibilities for purchased parts and preparing Control Drawings.

**WORK INSTRUCTIONS**

1. **REVIEW RESPONSIBILITY**

All Standards Engineering personnel, responsible for IURs (Item Usage Requests), selecting preferred parts and documenting preferred parts, have the responsibility to ensure that this document is correct and kept up to date. This responsibility includes the Manager of Standards Engineering, Team Leaders, all Standards Engineers and support personnel.

2. **IMPLEMENTATION RESPONSIBILITY**

The implementation of this document is primarily the responsibility of each Standard Engineer. The Standard Engineer has the responsibility to follow the procedures herein for each IUR he/she processes. It is also the responsibility of all management, team leaders and support personnel working within the Standards department to verify compliance with this Work Instruction.

3. **BACKGROUND**

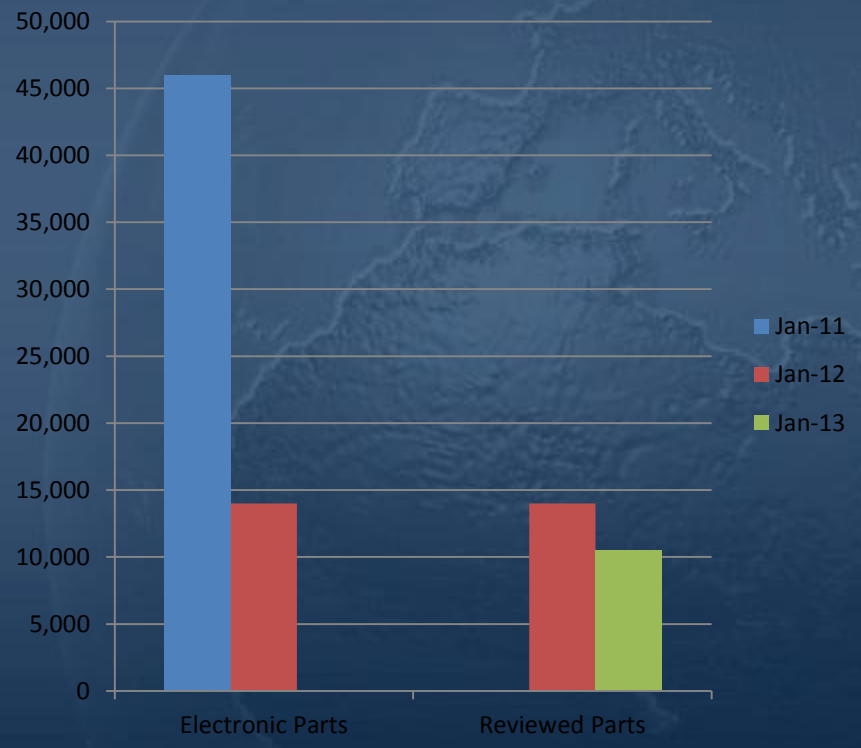
In documenting purchased parts, it is extremely important that the Most Preferred Part within a given functionality is identified with each new part request. The maintenance of a part documented and put





To create and maintain an effective PPL requires a paradigm shift focusing on continuous performance improvement (CPI).

# Aggressive Goals and Timetables



# Visibility of Real-Time Metrics

| | WEEK ENDING | 16-Mar | 23-Mar | 30-Mar | 6-Apr | 13-Apr | PC-3 GOAL |
|---|---|---|---|---|---|---|---|
| COMMODITY ENGINEER | GOAL: # PC-3's ASSIGNED TO MOVE BY 1 JUNE 2012 | 0 | 563 | 584 | 530 | 584 | 5852 |
| | ACTUAL - # MOVED (# PNs) | 0 | 413 | 608 | 893 | 432 | 5852 |
| | CUMULATIVE TOTAL | 0 | 249 | 498 | 1426 | 1426 | NA |
| | % COMPLETE | 0% | 3% | 7% | 20% | 20% | 20% |
| COMMODITY ENGINEER | GOAL: # OF PART TYPE TAXONOMIES ASSIGNED TO REVIEW | 0 | 29 | 29 | 26 | 33 | 342 |
| | # OF TAXONOMIES DEFINED / REDEFINED w Critical Attributes I'd (in components dB) (# PNs) | 0 | 2 | 0 | 10 | 0 | 342 |
| | CUMULATIVE TOTAL | 0 | 2 | 2 | 12 | 12 | NA |
| | % COMPLETE | 0% | 1% | 1% | 4% | 4% | 100% |
| DATA ANALYST (tbd) | GOAL: DEFINE # MISSING & WRONG CRITICAL ATTRIBUTES | TBD | | | | | |
| CO-OP ATTRIBUTES TEAM | GOAL: # OF CRITICAL ATTRIBUTES TO BE POPULATED | 0 | 0 | 0 | 0 | 0 | 1 |
| | # CRITICAL ATTRIBUTES POPULATED (5 8 GOAL) | 0 | 0 | 0 | 0 | 0 | 1 |
| | CUMMELATIVE TOTAL | 1 | 3 | 6 | 10 | 15 | NA |
| | % COMPLETE | 100% | 300% | 600% | 1000% | 1500% | 100% |
| COMMODITY ENGINEER | GOAL: # PNs ASSIGNED TO REVIEW LOWEST FUNCTIONALITY & ADJUST PCs | 0 | 1833 | 1838 | 1835 | 1836 | 19072 |
| | # OF PN's REVIEWED (#PNs) | 0 | 1489 | 3255 | 1489 | 3313 | 12384 |
| | CUMULATIVE TOTAL | 0 | 1489 | 4744 | 6233 | 10146 | NA |
| | % COMPLETE | 0% | 8% | 25% | 33% | 53% | 100% |

# Open Communications with Design Engineering and Program Management

Without open communications and the trust that goes with it, a PPL would be impossible to create and maintain.

# Flexibility

| Chk | Suffix | Suffix Description | Preferred Code |
|---|---|---|---|
| ☐ | 000 | ST PLUG TO ST PLUG 420 IN | 5- Restricted, Not Allowed |
| ☐ | 001 | ST PLGS INT RUGG & W/H, 144 IN | 5- Restricted, Not Allowed |
| ☐ | 002 | ST PLUG TO ST BK JK 41 IN | 4- Sustaining |
| ☐ | 003 | ST PLUG TO ST PLUG 240 IN | 5- Restricted, Not Allowed |
| ☐ | 004 | ST PLGS INT RUGG & WH,103.4 IN | 5- Restricted, Not Allowed |
| ☐ | 005 | ST PLUG TO ST BK JK, 80 IN | 1- Most Preferred |
| ☐ | 006 | ST PLG TO BHJ, INT RUGG, 80 IN | 4- Sustaining |
| ☐ | 007 | ST PLUG TO ST PLUG, 110 IN | 1- Most Preferred |
| ☐ | 008 | ST PLG TO RT ANG PLG, RUGG | 1- Most Preferred |
| ☐ | 009 | ST PLUGS, INT RUGG, 1182 IN | 4- Sustaining |
| ☐ | 010 | ST PLUGS, INT RUGG, 1418 IN | 4- Sustaining |
| ☐ | 011 | ST PLGS, BRASS ARMORED, 36 IN | 4- Sustaining |
| ☐ | 012 | STR PLGS, 2 EA CAP & CH, 46 IN | 5- Restricted, Not Allowed |
| ☐ | 013 | STR PLGS, 1 EA CAP & CH, 60 IN | 4- Sustaining |
| ☐ | 014 | STR PLG TO STR JACK , 60 IN | 5- Restricted, Not Allowed |
| ☐ | 015 | STR PLGS, 2 EA CAP & CH, 12 IN | 4- Sustaining |
| ☐ | 016 | STR PLGS, 2 EA CAP & CH, 75 IN | 4- Sustaining |
| ☐ | 017 | ST PLUG TO ST PLUG 24 IN | 1- Most Preferred |
| ☐ | 018 | ST PLUG TO ST JACK, 72 IN | 4- Sustaining |
| ☐ | 019 | ST PLG TO 4 HOLE PNL JK, 30 IN | 4- Sustaining |

To create and maintain an effective PPL requires flexibility in the way we address each opportunity to identify and reduce our PPL.

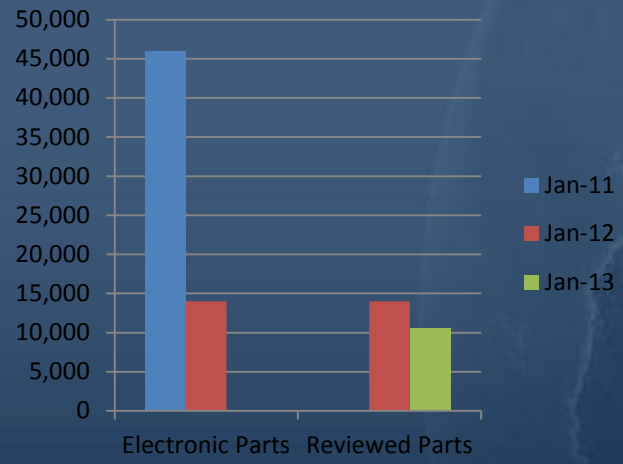
A Highly Refined Taxonomy to compare like parts within a given functionality at the finest degree of granularity


In order to select the most preferred part within a given functionality, it helps to refine the taxonomy of our parts to the finest degree of granularity in order to compare - not necessarily form or fit but definitely function.

These are steps we employed:

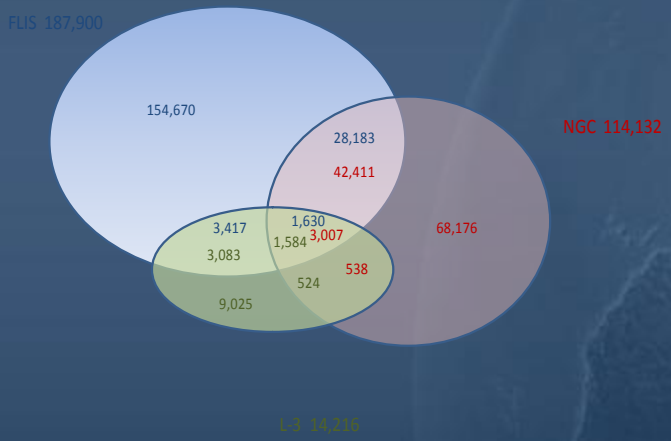
* Aggressive queries and filters to eliminate those parts not recently included in parts lists, BOMs and initial releases;
* Implement reductions found possible through participation in the DLA/LMI Data Sharing Initiative;
* Continuous review of all active functionally equivalent parts with each part query;
* Complete review of all remaining Preferred Parts by functionality over a period of 15 months; and
* Continuous Performance Improvements (CPI).

Aggressive queries and filters to eliminate those parts not recently included in parts lists, BOMs and initial releases;




Last year's goal was to reduce 46,000 electronic parts to 14,000. The goal for this year is to review 70% of the remaining parts and complete the complete review by April of 2013.

# Implement reductions found possible through participation in the DLA/LMI Data Sharing Initiative;




Because of the findings of the Data Sharing Demonstration we were able to implement the following reductions:

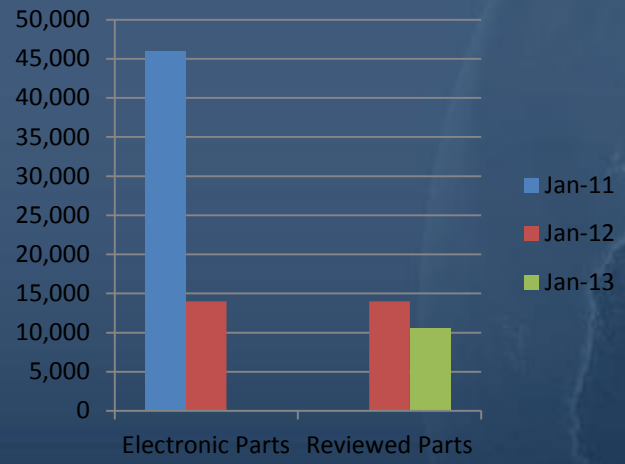
1.) Connectors - Reduce PC-1's from 6008 to 1558.

2.) Fasteners - Reduce PC-1's from 2036 to 529.

Continuous review of all active functionally equivalent parts with each part query;

"This here bell was made to tell that parts not needed can go to HELL!"

# Complete review of all remaining Preferred Parts by functionality over a period of 15 months




It is anticipated that nearly 3600 man-hours will be required for the complete review of the remaining 14,000 preferred parts.

# Continuous Performance Improvements (CPI).

Over the past year many initiatives have been undertaken in order to reduce our PPL. As we go forward many more initiatives will be undertaken as we gain more experience and understanding of those items already documented and their replacements.

We have already realized and will continue to realize:

* Cost savings alone save $9400 over the life of each part not introduced to our BOM (Coopers and Lybrand 1998);
* Reduction in Counterfeit Issues; and
* Actively identified and minimized DMS issues for follow-on contracts.

Cost savings alone save $9400 over the life of each part not introduced to our BOM (Coopers and Lybrand 1998)

In the six months since instituting the Bell Ringing and the PPL Work Instruction (WI-20), Standards Engineers have avoided the creation of 56 parts thus saving L-3 $532K!

# Actively identify and minimize DMS issues for follow-on contracts.

| Unkr | Othr | Total | |
|---|---|---|---|
| 83 | 2716 | 3203 | L3 Part Totals: |

| Stat | SC | PC | Description | Description 2 | SE LTB | Mfr Part | Mfr | SE Life | SE Par |
|---|---|---|---|---|---|---|---|---|---|
| U | AV | 5 | DETECTOR, RADIO FREQUENCY-- DIODE, 1-2 GHZ, 300 MV | DIODE, 1-2 GHZ, 300 MV/MW | | ACTP1525N | ADVANCED CONTROL COMPONEN | Unknown | Unknow |
| U | AV | 5 | POWER SUPPLY -- AC TO DC SWITCHER | 15 VOLTS, 1 AMP | | FAW15-1K-24S8 | KEPCO INC. | Not Revie | Not Revi |
| U | AV | 5 | POWER SUPPLY -- AC TO DC SWITCHER | 5 VOLT, 3AMP, 15 WATT | | FAW5-3K | KEPCO INC. | Not Revie | Not Revi |
| U | AV | 5 | MIXER STAGE, FREQUENCY-- RF=LO= 5-500MHZ,IF=DC= | RF=LO= 5-500MHZ,IF=DC-500MHZ | | LRFMS-1-SM6 | RF PRIME | Unknown | Unknow |
| U | ZV | 5 | RECEIVER, LIGHT SIGNAL -- 16 PIN DIP PACKAGE | SMA CONNECTOR, PB- | | DTL-125-W-RX-SMA | OPTICAL COMMUNICATION PDTS IN | 3-4 years | Active |
| U | AV | 5 | TRANSMITTER, LIGHT SIGNAL -- 16 PIN DIP PACKAGE | 125MB/S, SMA CONN, RL1 | | 2035-0128 | OPTICAL COMMUNICATION PDTS IN | Unknown | Unknow |
| | | | | | | DTL-125-W-TX-SMA | OPTICAL COMMUNICATION PDTS IN | 3-4 years | Active |
| U | SV | 5 | COIL, RADIO FREQUENCY -- CHIP SURFACE MOUNT, 115X1 | 10.0 UH, 2.95 OHMS, 300 MADC | | 1008LS-103XKBC | COILCRAFT INCORPORATED | Not Revie | Not Revi |
| U | AV | 5 | ARRESTOR, ELECTRICAL SURGE | | | SPA-100 | VISHAY DALE ELECTRONICS INC. C | Not Revie | Not Revi |
| U | AV | 5 | RESISTOR, VOLTAGE SENSITIVE | | | V56LA20B | GENERAL ELECTRIC CO | Obsolete | Obsolete |
| U | AV | 4 | PRIMER,ZINC CHROMATE | 1 GAL. CAN | | T446 | RAYTHEON CO MICROWAVE AND E | Not Revie | Not Revi |
| U | AV | 5 | PLATE, PHOTOGRAPHIC -- PHOTOSENSITIVE ANODIZE | BLACK SATIN/NATURAL AL,.063THK | | 5603 | HORIZONS INCORPORATED DBA IN | Not Revie | Not Revi |
| U | AV | 5 | PLATE, PHOTOGRAPHIC -- PHOTOSENSITIVE ANODIZE | BLACK SATIN/NATURAL AL,.003THK | | 2612 | HORIZONS INCORPORATED DBA IN | Not Revie | Not Revi |
| U | AV | 5 | PLATE, PHOTOGRAPHIC -- PHOTOSENSITIVE ANODIZE | BLACK SATIN/NATURAL AL,.032THK | | 5361 | HORIZONS INCORPORATED DBA IN | Not Revie | Not Revi |
| U | AV | 5 | PLATE, PHOTOGRAPHIC -- PHOTOSENSITIVE ANODIZE | BLACK SATIN/NATURAL AL,.020THK | | 5125 | HORIZONS INCORPORATED DBA IN | Not Revie | Not Revi |
| U | AV | 5 | NAVIGATION SET, SATELLITE DATA --RACK MOUNT GPS | GPS W/2 66-378 & 2 86-379 | | 151-306-143 | TRUETIME INC | Not Revie | Not Revi |
| U | AV | 5 | SOCKET,ELEC,CONN -- KIT | FOR MICRO PKG,LONG | | 47707 | VICOR CORPORATION | Not Revie | Not Revi |
| U | AV | 5 | POWER SUPPLY--DC TODCCONVERTER MODULE,24 VDC | 5VDC, 20A, MICRO (IND), RL0 | | VI-710255 | VICOR CORPORATION | Not Revie | Not Revi |
| U | SV | 5 | OSC, NON-CRYSTAL CONTROLLED -- VCO, 16-3.2GHZ,0-20 | -55 TO + 85 DEG C, 15VDC INPUT | | HV77T-12 | REMEC INC | Unknown | Active |
| U | AV | 5 | CAPACITOR,FXD,TANT,ALUM--RADIAL 2020 SERIES | 1200UF,20%,100VDC,BRKT, RLS | | 2020D128X0100BS + | VISHAY INTERTECHNOLOGY INC. | Not Revie | Not Revi |
| U | AV | 4 | CONN, P.C. ELEC-- SHROUD, THRU HOLE MOUNT | SHROUD, THRU HOLE MT | | 907545 | ERNI COMPONENTS INC | | Not Revi |
| U | AV | 5 | COMPUTER,DIGITAL--SINGLE-BOARD PPC750 | PPC750, 0 TO + 50 DEG C | | SYME-173-000 | DY 4 SYSTEMS INC DBA CURTISS V | Not Revie | Not Revi |
| U | AV | 5 | OSCILLATOR,CLOCK-- 5V,SMT 45MA | 16MHZ, -40+85C, RL1 | 6/15/2006 | JITO-2-DCSAM-16.0 | FOX ENTERPRISES INC | Obsolete | Obsolete |
| U | AV | 5 | OSCILLATOR,CLOCK-- 5V,SMT 45MA | 40MHZ, -40+85C, RL1 | 6/15/2006 | JITO-2-DCSAM-40.0 | FOX ENTERPRISES INC | Obsolete | Obsolete |
| U | AV | 5 | NAVIGATION SET, SATELLITE SIGNALS--4X5 PPS GPS R | 4X5 PPS GPS RECEIVER | | 23577-04 | TR NAVIGATION INC DBA TRIMBLE | Not Revie | Not Revi |
| U | AV | 4 | DISK DRIVE UNIT -- WIDE SCSI LOW-VOLTAGE DIFFERENT | LOW-VOLTAGE DIFF,9.1 GB MIN | | ST318437LW | SEAGATE TECHNOLOGY | Not Revie | Not Revi |
| U | AV | 4 | DISK DRIVE UNIT -- WIDE SCSI LOW-VOLTAGE DIFFERENT | LOW-VOLTAGE DIFF,36 GB MIN | | STXXXXXXLW W | SEAGATE TECHNOLOGY | | Not Revi |
| U | AV | 5 | CCA -- IP CARRIER CARD AND IP MODULES | MODULES, CARRIER CARD ONLY | | DMV-209-200 | DY 4 SYSTEMS INC DBA CURTISS V | Not Revie | Not Revi |
| U | AV | 5 | CCA -- IP CARRIER CARD AND IP MODULES | MODULES, 16-BIT ADC IND PACK | | IP4-660-200 | DY 4 SYSTEMS INC DBA CURTISS V | Not Revie | Not Revi |
| U | AV | 5 | CCA -- IP CARRIER CARD AND IP MODULES | MODULES,DIGITAL I/O IP, 2.4V | | IP4-206-220 | DY 4 SYSTEMS INC DBA CURTISS V | Not Revie | Not Revi |
| U | AV | 5 | INTERFACE UNIT, ADP--CISCO PIX FIREWALL KIT | FIREWALL KIT | | 7191930-000 | CISCO SYSTEMS INC. DBA CISCO S | | Not Revi |
| U | AV | 5 | COMPUTER, DIGITAL -- SINGLE- BOARD, ULTRASPARC 2E | BOARD,ULTRASPARC 2E, VME64X | | 31-652023-0XP | THEMIS COMPUTER DBA THEMIS | Not Revie | Not Revi |
| U | R | 5 | SOFTWARE W/LICENSE--SOLARIS 8 FOR ULTRASPARC IIE | APPLIANCE RTU, 1, 2 CPUS | | 103030-101 | THEMIS COMPUTER DBA THEMIS | Not Revie | Not Revi |
| U | AV | 5 | DISPLAY UNIT - 21 INCH FLAT | 21IN,MON W/FLE,VGLASS,DESKSTAND | | A21LUXG-UO-SSX | 2 MICROSYSTEMS INC. | | Not Revi |
| U | AV | 5 | IC,LIN,VOLT REG,POS,LM117HVH BCYP003XAB | BCYP003XAB | | LM117HVH | | 8-10 years | Active |
| | | | | | | LM117HVH | | 8-10 years | Active |
| A | V | 4 | IC,LIN,VOLT REG, 117HVK | | | 7703402YA | DEFENSE ELECTRONICS SUPPLY ( | Obsolete | Obsolete |
| U | AV | 5 | ENAMEL -- SEMIGLOSS, TT-E-529 | BLACK, FED-STD-595, NO 27038 | | | | | |
| U | AV | 5 | CAPACITOR, FIXED, PLASTIC DIEL 1.0 UF, 5 PCT, 240VAC/40 | 1.0 UF, 5 PCT, 240VAC/400VDC | | | | | |
| U | AV | 5 | SWITCH,TOGGLE--ROCKER,DIP,SPST 8 STATION,25MA,24 | 8 STATION,25MA,24VDC | | | | | |
| A | PV | 4 | IC, DGTL--(MC629) DELTA MODULATION CODEC, 5V | 64KBPS, PLCC24, -40+85 C, RL3 | | MX629LH | CML MICROCIRCUITS USA INC. | Unknown | Unknow |
| A | PV | 5 | IC,DGTL-- BIDIRECTIONAL RCVR, (16245), 16-BIT, 5V | TSSOP-48, -40+85C, RL4 | | IDT74FCT16245ATP | INTEGRATED DEVICE TECHNOLOG | Obsolete | Obsolete |
| | | | | | | IDT74FCT16245ATP | INTEGRATED DEVICE TECHNOLOG | 11-15 years | Active |
| | | | | | | IDT74FCT16245TP/ | INTEGRATED DEVICE TECHNOLOG | Obsolete | Obsolete |
| A | PV | 4 | MICROCIRCUIT, LINEAR--ADC0808 8-BIT A/D CONVERTER 2 | -40 TO + 85 DEG C, PLASTIC | | ADC0808CCV | NATIONAL SEMICONDUCTOR CORF | 5-7 years | Active |
| A | PV | 5 | MICROCIRCUIT,DIGITAL--723651 2KX36 CMOS SYNC FIFO, | 0 TO + 70 DEG C,20 NSEC,PLASTIC | | IDT723651L20PF | INTEGRATED DEVICE TECHNOLOG | 8-10 years | Active |
| I | S | 5 | CAPACITOR, FIXED, TANTALUM-- CHIP, LOW PROFILE | 10.0UF, 10%, 16VDC, 3528, RL2 | | TAJB106K016R | AVX CORP | Not Revie | Not Revi |
| I | V | 5 | CAPACITOR,FIXED,ELECTROLYTIC-- | 4700UF,20%,200VDC,NO SLEEVE,RL0 | | MLF401M200EK0C | CORNELL-DUBILIER MARKETING IN | Not Revie | Not Revi |
| I | BG | 5 | CCA - COMSEC INTERFACE | COTS KEY HOLD BAT TIMING/MPRV | | | | | |
| I | S | 4 | PRIMER COATING,EPOXY-POLYAMIDE CHROMATED,STAN | CHROMATED,STANDARD PIGMENTS,GL | | | | | |
| A | V | 5 | DELAY LINE -- FIXED GULL WING | 2.5 NS +/-50PS, -40+85C, RL2 | 12/3/2007 | GL/LSMS260S | THIN FILM TECHNOLOGY CORPOR | 1-2 years | Prelimin |
| A | S | 4 | MICROCIRCUIT,LINEAR--LM2341/31 POSITIVE VOLTAGE REC | 5V TO -26.3,LEAD MNT, PLASTIC | | LM2931CS | NATIONAL SEMICONDUCTOR CORF | 11-15 year | Active |
| A | Y | 4 | OSC, NON-CRYSTAL CONTROLLED-- 270-300MHZ,0.5-4.5V | 5.25DBM OUT,-40TO+85 DEG C,MET | | V28SME01 | | Unknown | Unknow |
| | | | | | | V28SME01 | 2-COMMUNICATIONS | 8-10 years | Unknow |
| | | | | | | V28SME01-LF | 2-COMMUNICATIONS | Unknown | Unknow |
| | | | | | | V28SME01 | | Unknown | Unknow |
| I | PV | 5 | MICROCIRCUIT,LINEAR--TLC2932I PHASE-LOCKED LOOP,I | -20 TO + 75 DEG C,TSOP, PLASTIC | | TLC2932IPWR | TEXAS INSTRUMENTS INCORPORA | Unknown | Unknow |
| A | PV | 5 | MICROCIRCUIT,LINEAR--LMC6484 RAIL TO RAIL INP & OUT | LOW POWER, CMOS, QUAD, PLASTIC | | LMC6484AIM | NATIONAL SEMICONDUCTOR CORF | 8-10 years | Active |
| A | S | 4 | MICROCIRCUIT,DIGITAL--AD8561 UNTRAFAST COMPARATO | -40 TO + 85 DEG C, PLASTIC | | AD8561AR | ANALOG DEVICES INC. | Unknown | Unknow |
| A | S | 4 | SEMICONDUCTOR DEVICE -- SCHOTTKY DIODE | 50MA, 75MV, SMT PLASTIC | | SMS3322-001 | ALPHA INDUSTRIES INC | Unknown | Unknow |
| A | PV | 4 | IC, LINEAR--HCPL0631 TTL OPTOCOUPLER, 5V | SOIC-8, -40 TO + 85 DEG C | | HCPL0631 | AVAGO TECHNOLOGIES U.S. INC | 5-7 years | Active |

By minimizing those preferred parts set aside only for sustainment (PC-4), we will reduce or eliminate those DMS and counterfeit issues that are allowed to be integrated into our fielded systems.

# Reduction in Counterfeit Issues

It is too early to quantify the impact of having an effective PPL will have on the mitigation of counterfeit issues. Nonetheless there is no question that as we strive diligently to use only preferred parts in our new (and to a degree our sustaining) designs, we will dramatically mitigate the possibility of integrating counterfeit parts into our product for the warfighter.

By maintaining an effective PPL, we will:

- Improve the Quality of New Designs;
- Minimize Costs and Avoid Duplications;
- Minimize Lead Times;
- Minimize Vanished Vendor (DMS) issues; and
- Minimize the Risk of Including Counterfeits in Product.

L3

Communication Systems-West